

# FPGA Software Installation and Firmware Update Instructions

AMD Versal Plus Ryzen Mini-ITX Board VPR-4616-MB VPR-5050-MB

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# SAPPHIRE - Embedded+ Initial Platform Install & Config

This page captures the steps to bring-up the <u>Embedded+</u> platform configuration for the x86 Host and Versal SoC device enablement.

## Hardware Setup:

Embedded+ system is comprised of a Versal SoC and a Ryzen SoC. The primary interfaces between these SoCs are PCIe and JTAG. Users interact with the Ryzen SoC through conventional means such as a keyboard and monitor, or via SSH once Linux is installed. Two UART ports are connected to the Versal SoC for debugging purposes only; they are not intended as the primary interface for Embedded+.

This documentation assumes the user possesses an Embedded+ system with OSPI pre-programmed at the factory, as this is the standard shipping configuration. The user will install Linux on the Ryzen X86 first, enabling subsequent interfacing with the Versal SoC.

# x86 Host OS

This section guide user to install Linux on X86. The Embedded+ platform's X86 host XRT driver for Embedded+ has been validated with Ubuntu 22.04 OS and the XRT Ubuntu library is aligned with the GA 5.15 Linux kernel. Thus, if the default installed Ubuntu image is using a later kernel, the following steps are required to update the x86 Ubuntu OS to be aligned:

() These steps are necessary for VPR-4616/5050-MB only. VPR-4616/5050-SYS has preloaded software and this section is not necessary.

- 1. Install the x86 host OS. Instructions and the image are available directly from Canonical:
  - a Requires a USB stick, keyboard, monitor, mouse, and ethernet connection
  - b Install instructions: https://ubuntu.com/tutorials/install-ubuntu-desktop#1-overview
  - c OS image download: <u>https://releases.ubuntu.com/jammy/</u>
- 2. Once the x86 host OS is installed and booted from its SSD. Update the kernel to the 5.15 generic kernel with these steps
  - a. Install the generic kernel

#### Generic kernel install

sudo apt install linux-image-generic sudo apt install -f

b. Replace the "GRUB\_DEFAULT" string in: /etc/default/grub with GRUB\_DEFAULT="Advanced options for Ubuntu>Ubuntu, with Linux 5.15.0-###-generic".

Note that the ### in the string above needs to be aligned with the generic kernel number used in the install of the previous step

c. Update grub config & reboot:

Kernel update
sudo update-grub
sudo reboot now

3. Validate the kernel update using:

Validate kernel	
uname -r	

- 4. Install the XRT drivers on the x86 host. The Embedded+ platform support is now only be built in the 2024.1 XRT builds. This is NOT required to be aligned to the Embedded+ 2023.2 HW shell designs.
  - a Get the latest XRT *xrt\_202410.<date>\_22.04-amd64-xrt.deb* from the automated builds at: <u>https://www.xilinx.com/member/forms/download/xef.html?filename=xrt\_202410.2.17.326</u> <u>\_22.04-amd64-xrt.deb</u>

(i) xrt version

Ensure that the XRT version is 2.17.306 or later.

- b Move xrt.deb package to the Embedded+ platform running Ubuntu 22.04
- c Install the 5.15 headers. Use the ### associated with the generic kernel installed.

#### Generic kernel headers

sudo apt install linux-headers-\$(uname -r)

d Install the xrt package with:

#### **Install XRT driver**

sudo dpkg -i xrt\_202410.<date>\_22.04-amd64-xrt.deb

e The previous step may take some time as it will build the driver locally on target. After it completes verify that the drivers are installed correctly using: *lsmod* 

lsmod		
d773@d773-deskto	p:~/Desktop/7	0773 Linux test tools\$ lsmod   grep xocl
xocl	2220032	
libcrc32c	12288	2 xclmgmt,xocl
drm	765952	15 gpu_sched,drm_kms_helper,drm_suballoc_helper,xocl,drm_display_helper,drm_buddy,amdgpu,drm_ttm_helper,ttm,amdxc
d773@d773-deskto	p:~/Desktop/7	D773 Linux test tools\$ lsmod   grep xclmgmt
xclmgmt	1294336	0
libcrc32c	12288	2 xclmont.xocl

- 5. Install the Embedded+ VE2302 "base" device package
  - a Get the latest base package from

https://www.xilinx.com/member/forms/download/xef.html?filename=xrt-emb-plus-ve2302base 1.0.deb

b Move package to the Embedded+ platform.

#### c Install with:

#### Install VE2302 base design files

sudo dpkg -i xrt-emb-plus-ve2302-base\_1.0.deb

#### 6. Install the Embedded+ VE2302 XRT platform test bitstream packages

a Get the latest test bitstream packages from:

xrt-verify-test-ve2302\_1.0.deb: https://www.xilinx.com/member/forms/download/xef.html?filename=xrt-verify-testve2302\_1.0.deb

#### xrt-bandwidth-dma-test-ve2302\_1.0.deb

https://www.xilinx.com/member/forms/download/xef.html?filename=xrt-bandwidth-dmatest-ve2302 1.0.deb

#### xrt-aie-test-ve2302\_1.0.deb

https://www.xilinx.com/member/forms/download/xef.html?filename=xrt-aie-testve2302 1.0.deb

- b Move the packages to the Embedded+ platform.
- c Install with:

#### Install XRT test bitstreams

sudo dpkg -i xrt-verify-test-ve2302\_1.0.deb sudo dpkg -i xrt-bandwidth-dma-test-ve2302\_1.0.deb sudo dpkg -i xrt-aie-test-ve2302\_1.0.deb

#### 7. Install the Versal APU SW package

- a Get the latest APU SW package from: <u>https://www.xilinx.com/member/forms/download/xef.html?filename=xrt-apu-linux-ve2302\_1.0.deb</u>
- b Move the package to the Embedded+ platform.
- c Install with:

#### Install Versal APU SW

sudo dpkg -i xrt-apu-linux-ve2302\_1.0.deb

8. Reboot the system

# **On-target Self Test**

The following are self-test that the user can run to test that the Versal and x86 host are set-up and configured correctly.

#### **PCIe Connectivity**

Use *lspci* to inspect if Versal device is present on PCIe bus.

lspci	
d773@d77 01:00.0	73-desktop:~/Desktop/7D773 Linux test tools\$ lspci -vd 10ee: Processing accelerators: Xilinx Corporation Device 5700 Subsystem: Xilinx Corporation Device 000e Flags: bus master, fast devsel, latency 0, IRQ 67, IOMMU group 9 Memory at 1fe0000000 (64-bit, prefetchable) [size=256M] Memory at 1ff8040000 (64-bit, prefetchable) [size=256K] Capabilities: <access denied=""> Kernel driver in use: xclmgmt Kernel modules: xclmgmt</access>
01:00.1	Processing accelerators: Xilinx Corporation Device 5701 Subsystem: Xilinx Corporation Device 000e Flags: bus master, fast devsel, latency 0, IRQ 67, IOMMU group 10 Memory at 1ff8000000 (64-bit, prefetchable) [size=256K] Memory at 1ff0000000 (64-bit, prefetchable) [size=128M] Capabilities: <access denied=""> Kernel driver in use: xocl Kernel modules: xocl</access>

#### **XRT Tests**

The XRT "validate" tests are a set of PL/AIE design used to exercise basic functionality of the system. They have been installed with xrt\*test\*.deb packages.

Source the XRT tools:

Source XRT	
source /opt/xilinx/xrt/setup.sh	

Now the system is set up to run tests via xbutil command: verify, dma, mem-bw and aie. Instructions in following sections:

#### **XRT Platform Inspection**

Use XRT xbmgmt to see platform information.

<b>XRT</b> Platform	Inspection
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xbmgmt examine

xbmgmt capture				
System Configuration				
OS Name	: Li	านx		
Release	: 6.	5.0-41-generic		
Version	: #4	<pre>L~22.04.2-Ubuntu SMP PREEMPT_DYNAMIC Mo</pre>	n Jun 3 11:32:5	5 UTC 2
Machine	: x8	5_64		
CPU Cores	: 4			
Memory	: 58	53 MB		
Distribution	: Ubi	untu 22.04.4 LTS		
GLIBC	: 2.	35		
Model	1			
BIOS vendor	: Amo	erican Megatrends International, LLC.		
BIOS version	: 5.3	24		
XRT				
Version	: 2.	17.326		
Branch	: 20	24.1		
Hash	: 85	5be14f8ad700619aa836244352b52d20f082a5		
Hash Date	: 20	24-06-10 01:22:20		
XOCL	: 2.	17.326, 856be14f8ad700619aa836244352b52	d20f082a5	
XCLMGMT	: 2.	17.326, 856be14f8ad700619aa836244352b52	d20f082a5	
Firmware Version	: N//	A		
Devices present				
BDF : SI	nell	Logic UUID	Device ID	Device Ready*
[0000:01:00.0] : er	nb-plus	00000000-0000-0000-0000-000079DB078F	mgmt(inst=256)	Yes
* Devices that are no	ot ready	y will have reduced functionality when	using XRT tools	

#### **Verify Test**

The "Verify" test is a simple "hello world" application for testing core ability to download a user kernel captured as an xclbin and have an expected data transfer read back from that kernel "Hello World".

Run the test:	 
Verify Test	
xbutil validate -r verify -dverbose	
Expected output:	

```
Verify test results
Validation completed
Verify Test
Verbose: Enabling Verbosity
Validate Device
                         : [0000:01:00.1]
    Platform
                         : emb-plus
    SC Version
                         : 0.0.0
    Platform ID
                         : 00000000-0000-0000-0000-000079DB078F
Test 1 [0000:01:00.1] : verify
                        : Run 'Hello World' kernel test
    Description
                         : [PASSED]
    Test Status
       -----
```

#### **DMA Test**

The "DMA" test is a simple DMA test that transfer data between Versal and Ryzen using DMA on Versal.

Run the test:

DMA test	
xbutil validate -r dma -dverbose	

#### Expected output:

DMA test results	
Verbose: Enabling Verbo	sity
Validate Device	: [0000:01:00.1]
Platform	: emb-plus
SC Version	: 0.0.0
Platform ID	: 00000000-0000-0000-0000-000079DB078F
st 1 [0000:01:00.1]	: dma
Description	: Run dma test
Details	: Buffer size - '16 MB' Memory Tag - 'MC_NOC'
	Host -> PCIe -> FPGA write bandwidth = 2704.1 MB/s
	Host <- PCIe <- FPGA read bandwidth = 3504.2 MB/s
Test Status	: [PASSED]

#### **Bandwidth Test**

The "bandwidth" test runs a limited bandwidth test on DDR memory and PCIe data transfers.

Run the test:

#### Bandwidth test

xbutil validate -r mem-bw -d --verbose

#### Expected output:

Bandwidth test results		
Validation completed		
Bandwith Test		
Verbose: Enabling Verbos	sity	
Validate Device	: [0000:01:00.1]	
Platform	: emb-plus	
SC Version	: 0.0.0	
Platform ID	: 00000000-0000-0000-0000-000079DB078F	
Toot 1 [0000.01.00 1]		
Test I [0000:01:00.1]	; mem-bw	
Description	: Run 'bandwidth kernel' and check the throughput	
Details	: Throughput (Type: DDR) (Bank count: 1) : 19002.7 MB/s Throughput of Memory Tag: MC_NOC : 19002.7 MB/s	
Test Status	: [PASSED]	

#### AIE Test

The "aie" test runs a AIE tile functionality test.

Run the test:

```
AIE test
xbutil validate -r aie -d --verbose
```

#### Expected output:

AIE test results		
Validation completed		
AIE Test		
Verbose: Enabling Verbosi	ty	
Validate Device	: [0000:01:00.1]	
Platform	: emb-plus	
SC Version	: 0.0.0	
Platform ID	: 00000000-0000-0000-0000-000079DB078F	
Test 1 [0000:01:00.1]	: aie	
Description	: Run AIE PL test	
Test Status	: [PASSED]	

### **Debug Tools – Versal Serial Console**

The Versal serial console is connected to the Ryzen device on the motherboard. Therefore user can access the uart outputs from Ryzen. In Ubuntu, first download picocom:

#### Install picocom

sudo apt-get install picocom

Then user can access the com ports on commandline from Ubuntu:

#### **Connect to APU serial output:**

sudo picocom -b 115200 /dev/ttyUSB1

#### Connect to PLM/RPU serial output:

sudo picocom -b 115200 /dev/ttyUSB2

### Repositories

The following links are the sources of this example artifacts

https://github.com/Xilinx/emb plus vitis platforms/releases

https://github.com/Xilinx/emb-plus-examples/releases

# FPGA(Xilinx) F/W Update

Please follow the instruction of the link for update <u>GitHub – Xilinx/embpf-bootfw-update-tool</u>